



Cipherithm LLC

PCI PTS V3.x to V4.0 Gap Analysis, But WTF

A Cipherithm White Paper

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Revision Control

Version	Date	Editor	Change Description
1.00	Sept 1, 2013	SS	White paper release

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Introduction

The purpose of this white paper is to provide a gap analysis between the newly published PCI PTS POI security requirements version 4 to the previous version 3.1. This gap analysis is done from the PED manufacturer's point of view.

This white paper is meant to be an easy to read comparison between PCI PTS version 3.1 and the newly published PCI PTS version 4. Each requirement is listed side by side with our analysis. If you have heard the reports that there are few security changes between the two versions then this white paper will be of interest to you, Despite the fact that the text of each of the security requirement is mostly unchanged, there are modest changes, additions guidance to the corresponding vendor questionnaire and derived test requirements (DTRs).

This white paper points out the number of additional questionnaire questions and documentation that must be provided by a secure vendor to the lab. In some cases, there are a significant number of new tests that the lab is required to review.

Feel free to submit comments, questions, and editorial suggests to info@cipheritm.com

Please note the details of the DTR gap is not provided in this publically available white paper because the DTRs are provided under NDA. If you are interested in the detailed DTR differences please contact Cipherithm.

If you are reading this you must be interested in PTS security requirement. You are expected to review the details that follow. If you give up on the details, be sure to read the summary at the end.

Considerations

For those vendors that have been told that the change from version 3.1 to version 4 is minor you should do a complete review of your version 3.x approved product against the PTS version 4 requirements, including the impact of the DTRs. There are some areas that we believe will required hardware design modifications for most devices. Keep in mind that increasing security on any given design is very difficult, in some cases not possible without significant changes to the electronics and mechanical design. Then the question of making changes to the current design or creation of a new product line needs to be addressed.

We remember when PCI version 1 was first announced to replace the Visa PED approval program. This predates the founding of the PCI SSC as we know it today. The initiative was driven by MasterCard and Visa, an attempt to align the PED security requirements. The vendors at the time applauded the coordination of the two brands that relied on PIN security. However, in the initial meeting it was announced that since there was no major changes to the requirements, that six months after the announcement, all new products would be tested under the PCI version 1 requirements. The first PCI approved product was listed about 14 months after the initial announcement. The reason is took so long is that the new details of the questionnaire and DTRs required new security product design to meet the new security standard. Since that time we have been a bit more skeptical when we hear from the brands and PCI that new security requirements are minor, which is why we have published this white paper.

Keep in mind that to obtain a PCI PTS approval a 100% compliance report is needed. Even the smallest non-compliance issue can sabotage a product approval.

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Side by Side V3.1 / V4 Comparison

Evaluation Module 1: Core Requirements

A – Core Physical Security Requirements

Requirement Alignment Table

PCI PTS Version 3.1	PCI PTS Version 4	Notes
<p>Modular Security Requirement</p> <p>A1.1 <i>The device uses tamper-detection and response mechanisms that cause it to become immediately inoperable and result in the automatic and immediate erasure of any sensitive data that may be stored in the device, such that it becomes infeasible to recover the sensitive data. These mechanisms protect against physical penetration of the device by means of (but not limited to) drills, lasers, chemical solvents, opening covers, splitting the casing (seams), and using ventilation openings; and there is not any demonstrable way to disable or defeat the mechanism and insert a PIN-disclosing bug or gain access to secret information without requiring an attack potential of at least 26 per device for identification and initial exploitation, with a minimum of 13 for exploitation, exclusive of the IC card reader; and</i></p> <p><i>Note: The replacement of both the front and rear casings shall be considered as part of any attack scenario.</i></p>	<p>A1 <i>The device uses tamper-detection and response mechanisms that cause it to become immediately inoperable and result in the automatic and immediate erasure of any sensitive data that may be stored in the device, such that it becomes infeasible to recover the sensitive data. These mechanisms protect against physical penetration of the device by means of (but not limited to) drills, lasers, chemical solvents, opening covers, splitting the casing (seams), and using ventilation openings; and there is not any demonstrable way to disable or defeat the mechanism and insert a PIN-disclosing bug or gain access to secret information without requiring an attack potential of at least 26 per device for identification and initial exploitation, with a minimum of 13 for exploitation, exclusive of the IC card reader; and</i></p> <p><i>Note: The replacement of both the front and rear casings shall be considered as part of any attack scenario. All attacks shall include a minimum of ten hours attack time for exploitation.</i></p>	<p>The A requirement is no longer two parts, A1.1 and A1.2. The same requirements are now A1 and A2 respectively.</p> <p>The ten hour minimum for exploitation is finally included as a specific requirement. This has been a requirement for scoring the attack potential, but one would have to refer to the Technical FAQ.</p>

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PCI PTS Version 3.1	PCI PTS Version 4	Notes
<p>Modular Evaluation Vendor Questionnaire Total Questions: 9</p> <p>Modular Derived Test Requirements Total Tests: 6</p>	<p>Total Questions: 20</p>	<p>There are significant number questions that must be answered. Most are details of the design and require additional details be provided to the lab.</p> <p>There are two areas of concern based on PED designs.</p> <p><i>Q18 How the device (if used for PIN entry) is protected against placement of an external overlay—i.e., a secondary keypad on top of the existing keypad.</i></p> <p><i>Q20 How the device is protected from:</i></p> <ul style="list-style-type: none"> • Each side of the device • The back of the device • The front of the device <p>Significant change identified</p>
<p>Modular Security Requirement</p> <p>A1.2 <i>Failure of a single security mechanism does not compromise device security. Protection against a threat is based on a combination of at least two independent security mechanisms.</i></p> <p>Modular Evaluation Vendor Questionnaire Total Questions: 4</p> <p>Modular Derived Test Requirements Total Tests: 3</p>	<p>A2 <i>Failure of a single security mechanism does not compromise device security. Protection against a threat is based on a combination of at least two independent security mechanisms.</i></p> <p>Total Questions: 4</p>	<p>A1.2 is now A2.</p> <p>There are no additional questions for this requirement.</p> <p>New guidance provided</p>

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PCI PTS Version 3.1	PCI PTS Version 4	Notes
<p>Modular Security Requirement</p> <p>A2 <i>If the device permits access to internal areas (e.g., for service or maintenance), it is not possible using this access area to insert a bug that would disclose sensitive data. Immediate access to sensitive data such as PIN or cryptographic data is either prevented by the design of the internal areas (e.g., by enclosing components with sensitive data into tamper-resistant/responsive enclosures), and/or it has a mechanism so that accessing internal areas causes the immediate erasure of sensitive data.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 6</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 8</p>	<p>Total Questions: 0</p>	<p>This requirement was removed because it was redundant with other tests.</p>
<p>Modular Security Requirement</p> <p>A3 <i>The security of the device is not compromised by altering:</i></p> <ul style="list-style-type: none"> • <i>Environmental conditions</i> • <i>Operational conditions</i> <p><i>(An example includes subjecting the device to temperatures or operating voltages outside the stated operating ranges.)</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 3</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 4</p>	<p>A3 <i>The security of the device is not compromised by altering:</i></p> <ul style="list-style-type: none"> • <i>Environmental conditions</i> • <i>Operational conditions</i> <p><i>(An example includes subjecting the device to temperatures or operating voltages outside the stated operating ranges.)</i></p> <p>Total Questions: 6</p> <p>Total Tests: 10</p>	<p>Additional questions concerning the temperature detection and “glitch” detection and prevention.</p> <p>New guidance provided</p>

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PCI PTS Version 3.1	PCI PTS Version 4	Notes
<p>Modular Security Requirement</p> <p>A4 <i>Sensitive functions or data are only used in the protected area(s) of the device. Sensitive data and functions dealing with sensitive data are protected from modification without requiring an attack potential of at least 26 for identification and initial exploitation, with a minimum of 13 for exploitation, exclusive of the IC card reader, for identification and initial exploitation</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 6</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 4</p>	<p>A4 <i>Sensitive functions or data are only used in the protected area(s) of the device. Sensitive data and functions dealing with sensitive data are protected from modification without requiring an attack potential of at least 26 for identification and initial exploitation, with a minimum of 13 for exploitation, exclusive of the IC card reader, for identification and initial exploitation</i></p> <p>Total Questions: 11</p>	<p>Additional questions about the device design</p> <p>New guidance provided</p>
<p>Modular Security Requirement</p> <p>A5 <i>If PIN entry is accompanied by audible tones, then the tone for each entered PIN digit is indistinguishable from the tone for any other entered PIN digit</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 3</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 3</p>	<p>A11 <i>If PIN entry is accompanied by audible tones, the tone for each entered PIN digit is indistinguishable from the tone for any other entered PIN digit</i></p> <p>Total Questions: 3</p>	<p>No reason given for moving this requirement.</p> <p>Possible side channel</p>
<p>Modular Security Requirement</p> <p>A6 <i>There is no feasible way to determine any entered and internally transmitted PIN digit by monitoring sound, electro-magnetic emissions, power consumption or any other external characteristic available for monitoring—even with the cooperation of the device operator or sales clerk—without requiring an attack potential of at least 26 for identification and initial exploitation with a minimum of 13 for exploitation</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 6</p>	<p>A5 <i>There is no feasible way to determine any entered and internally transmitted PIN digit by monitoring sound, electro-magnetic emissions, power consumption or any other external characteristic available for monitoring—even with the cooperation of the device operator or sales clerk—without requiring an attack potential of at least 26 for identification and initial exploitation with a minimum of 13 for exploitation</i></p> <p>Total Questions: 6</p>	<p>Minor change to Q3 requiring more specific tests and results.</p>

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PCI PTS Version 3.1	PCI PTS Version 4	Notes
<p>Modular Derived Test Requirements</p> <p>Total Tests: 5</p>		<p> Identified as a possible red flag</p>
<p>Modular Security Requirement</p> <p>A7 <i>Determination of any PIN-security-related cryptographic key resident in the device, by penetration of the device and/or by monitoring emanations from the device (including power fluctuations), requires an attack potential of at least 35 for identification and initial exploitation with a minimum of 15 for exploitation</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 5</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 3</p>	<p>A6 <i>Determination of any PIN-security-related cryptographic key resident in the device, by penetration of the device and/or by monitoring emanations from the device (including power fluctuations), requires an attack potential of at least 35 for identification and initial exploitation with a minimum of 15 for exploitation</i></p> <p>Total Questions: 9</p>	<p>Additional information for the cryptographic processor is required for side channel attacks and how it is not possible to re-enable the programming and in-circuit testing features on a product device.</p> <p>New guidance provided</p>

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PCI PTS Version 3.1	PCI PTS Version 4	Notes
<p>Modular Security Requirement</p> <p>A8 <i>Note: If the POI device has a keypad that can be used to enter non-PIN data, the device must meet at least one of the following: A8, B16.1, B16.2, or E3.4.</i></p> <p><i>(Statements A8 and B16.1 are intended to be met by the vendor controlling the means of authorizing prompt changes. Statement B16.2 is an option that allows third parties to control the means of authorization. E3.4 is for all other unattended POI devices not meeting one of the aforementioned.)</i></p> <p><i>The unauthorized alteration of prompts for non-PIN data entry into the PIN entry key pad such that PINs are compromised, i.e., by prompting for the PIN entry when the output is not encrypted, cannot occur without requiring an attack potential of at least 18 per device for identification and initial exploitation with a minimum of 9 for exploitation</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 3</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 3</p>	<p>A7 <i>Note: If the POI device has a keypad that can be used to enter non-PIN data, the device must meet at least one of the following: A7, B16, or E3.4.</i></p> <ul style="list-style-type: none"> <i>A7 applies to any components or paths containing plaintext display signals between the cryptographic processor and display unit.</i> <i>B16 applies to devices that allow for updates of prompts or use cryptography to communicate with a display, whether performed by the vendor or the acquirer.</i> <i>E3.4 is appropriate for unattended devices that do not meet any of the aforementioned.</i> <p><i>The unauthorized alteration of prompts for non-PIN data entry into the PIN entry key pad such that PINs are compromised, i.e., by prompting for the PIN entry when the output is not encrypted, cannot occur without requiring an attack potential of at least 18 per device for identification and initial exploitation with a minimum of 9 for exploitation</i></p> <p>Total Questions: 4</p>	<p>The note here indicates the change made to requirement B16.</p> <p>Additional guidance in Q1 states that access to the display is in scope of this requirement and where prompts and what protections are there.</p> <p> Identified as a possible red flag</p>
<p>Modular Security Requirement</p> <p>A9 <i>The device provides a means to deter the visual observation of PIN values as they are being entered by the cardholder.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 2</p>	<p>A8 <i>The device provides a means to deter the visual observation of PIN values as they are being entered by the cardholder.</i></p> <p>Total Questions: 3</p>	<p>Additional documentation is required to be supplied to the lab.</p>

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PCI PTS Version 3.1	PCI PTS Version 4	Notes
<p>Modular Derived Test Requirements</p> <p>Total Tests: 4</p>		
<p>Modular Security Requirement</p> <p>A10 <i>It is not feasible to penetrate the device to make any additions, substitutions, or modifications to the magnetic-stripe reader and associated hardware or software, in order to determine or modify magnetic-stripe track data, without requiring an attack potential of at least 16 per device, for identification and initial exploitation, with a minimum of 8 for exploitation</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 4</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 5</p>	<p>A9 <i>It is not feasible to penetrate the device to make any additions, substitutions, or modifications to the magnetic-stripe reader and associated hardware or software, in order to determine or modify magnetic-stripe track data, without requiring an attack potential of at least 16 per device, for identification and initial exploitation, with a minimum of 8 for exploitation</i></p> <p>Total Questions: 6</p>	<p>Additional questions added to the questionnaire about the API used to collect the MSR data and how the device prevents skimming attacks including the path clear text MSR data travels inside the device.</p> <p> Identified as a possible red flag</p>
<p>Modular Security Requirement</p> <p>A11 <i>Secure components intended for unattended devices contain an anti-removal mechanism to protect against unauthorized removal and/or unauthorized re-installation. Defeating or circumventing this mechanism must require an attack potential of at least 18 per device for identification and initial exploitation, with a minimum of 9 for exploitation</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 7</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 6</p>	<p>A10 <i>Secure components intended for unattended devices contain an anti-removal mechanism to protect against unauthorized removal and/or unauthorized re-installation. Defeating or circumventing this mechanism must require an attack potential of at least 18 per device for identification and initial exploitation, with a minimum of 9 for exploitation</i></p> <p>Total Questions: 8</p>	<p>One new question about the use of cryptography to secure modular components and how replay and man-in-the-middle attacks are prevented.</p> <p>Important changes for UPT vendors</p>

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B – Core Logical Security Requirements

Requirement Alignment Table

PCI PTS Version 3.1	PCI PTS Version 4	Notes
<p>Modular Security Requirement</p> <p>B1 <i>The device performs a self-test, which includes integrity and authenticity tests as addressed in B4, upon start-up and at least once per day to check firmware, security mechanisms for signs of tampering, and whether the device is in a compromised state. In the event of a failure, the device and its functionality fail in a secure manner.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 4</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 4</p>	<p>B1 <i>The device performs a self-test, which includes integrity and authenticity tests upon start-up and at least once per day to check whether the device is in a compromised state. In the event of a failure, the device and its functionality fail in a secure manner. The device must reinitialize memory at least every 24 hours.</i></p> <p>Total Questions: 8</p>	<p>It is not clear what memory needs to be reinitialized or how this may be done. This almost appears to require a reboot.</p> <p>Additional information about the boot code and loading of firmware is required for lab review and evaluation. Seems there is a move to make a distinction of the security module performing self test versus vendor code and segmentation for meeting the same requirement.</p> <p>New guidance provided</p>
<p>Modular Security Requirement</p> <p>B2 <i>The device's functionality shall not be influenced by logical anomalies such as (but not limited to) unexpected command sequences, unknown commands, commands in a wrong device mode and supplying wrong parameters or data which could result in the device outputting the clear-text PIN or other sensitive data.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 6</p>	<p>B2 <i>The device's functionality shall not be influenced by logical anomalies such as (but not limited to) unexpected command sequences, unknown commands, commands in a wrong device mode and supplying wrong parameters or data which could result in the device outputting the clear-text PIN or other sensitive data.</i></p> <p>Total Questions: 10</p>	<p>The additional questions require more details about the logic used to process the API and what programming languages are used.</p>

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PCI PTS Version 3.1	PCI PTS Version 4	Notes
<p>Modular Derived Test Requirements</p> <p>Total Tests: 4</p>		New guidance provided
<p>Modular Security Requirement</p> <p>B3 <i>The firmware, and any changes thereafter, have been inspected and reviewed using a documented and auditable process, and certified as being free from hidden and unauthorized or undocumented functions.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 2</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 4</p>	<p>B3 <i>The firmware, and any changes thereafter, have been inspected and reviewed using a documented and auditable process, and certified as being free from hidden and unauthorized or undocumented functions.</i></p> <p>Total Questions: 4</p>	<p>Additional documentation for software design, testing and release is required for the device evaluation.</p> <p>New guidance provided</p>
<p>Modular Security Requirement</p> <p>B4 <i>If the device allows updates of firmware, the device cryptographically authenticates the firmware and if the authenticity is not confirmed, the firmware update is rejected and deleted.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 5</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 7</p>	<p>B4 <i>If the device allows updates of firmware, the device cryptographically authenticates the firmware and if the authenticity is not confirmed, the firmware update is rejected and deleted.</i></p> <p>Total Questions: 7</p>	<p>Additional questions about firmware loading and public/private key loading.</p> <p>New guidance provided</p>
<p>Modular Security Requirement</p>	<p>B4.1 <i>The firmware must support the authentication of applications loaded onto the terminal consistent with B4. If the device allows software application and/or configuration updates, the device cryptographically authenticates updates consistent with B4.</i></p>	<p>The additional B4.1 requirement was a SRED requirement and moved to a core requirement. Previously the PED only had to authenticate and test the integrity of firmware, now applications must also be authenticated and integrity tested.</p>

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PCI PTS Version 3.1	PCI PTS Version 4	Notes
<p>Total Questions: 0</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 0</p>	<p>Total Questions: 9</p>	<p>The questions for this requirement are meant to identify all components that have “firmware” that are required to meet the PCI PTS requirements. The device must also perform authentication for applications as well as the previous requirement to authentication firmware.</p> <p>New tests for application authentication</p>
<p>Modular Security Requirement</p> <p>B5 <i>The device never displays the entered PIN digits. Any array related to PIN entry displays only non-significant symbols, e.g., asterisks.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 2</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 4</p>	<p>B5 <i>The device never displays the entered PIN digits. Any array related to PIN entry displays only non-significant symbols, e.g., asterisks.</i></p> <p>Total Questions: 2</p>	<p>No new questions</p>
<p>Modular Security Requirement</p> <p>B6 <i>Sensitive data shall not be retained any longer, or used more often, than strictly necessary. Online PINs are encrypted within the device immediately after PIN entry is complete and has been signified as such by the cardholder, e.g., via pressing the enter button.</i></p> <p><i>The device must automatically clear its internal buffers when either:</i></p> <ul style="list-style-type: none"> • <i>The transaction is completed, or</i> • <i>The device has timed out waiting for the response from the cardholder or merchant.</i> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 10</p>	<p>B6 <i>Sensitive data shall not be retained any longer, or used more often, than strictly necessary. Online PINs are encrypted within the device immediately after PIN entry is complete and has been signified as such by the cardholder, e.g., via pressing the enter button.</i></p> <p><i>The device must automatically clear its internal buffers when either:</i></p> <ul style="list-style-type: none"> • <i>The transaction is completed, or</i> • <i>The device has timed out waiting for the response from the cardholder or merchant.</i> <p>Total Questions: 10</p>	<p>No new questions</p>

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PCI PTS Version 3.1	PCI PTS Version 4	Notes
<p>Modular Derived Test Requirements</p> <p>Total Tests: 4</p>		
<p>Modular Security Requirement</p> <p>B7 <i>Access to sensitive services requires authentication. Sensitive services provide access to the underlying sensitive functions. Sensitive functions are those functions that process sensitive data such as cryptographic keys, PINs, and passwords. Entering or exiting sensitive services shall not reveal or otherwise affect sensitive data.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 10</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 8</p>	<p>B7 <i>Access to sensitive services requires authentication. Sensitive services provide access to the underlying sensitive functions. Sensitive functions are those functions that process sensitive data such as cryptographic keys, PINs, and passwords. Entering or exiting sensitive services shall not reveal or otherwise affect sensitive data.</i></p> <p>Total Questions: 11</p>	<p>The one additional questions requires all methods to load keys into the device be identified.</p> <p>Modest impact</p>
<p>Modular Security Requirement</p> <p>B8 <i>To minimize the risks from unauthorized use of sensitive services, limits on the number of actions that can be performed and a time limit imposed, after which the device is forced to return to its normal mode.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 8</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 6</p>	<p>B8 <i>To minimize the risks from unauthorized use of sensitive services, limits on the number of actions that can be performed and a time limit imposed, after which the device is forced to return to its normal mode.</i></p> <p>Total Questions: 8</p>	<p>No new questions.</p> <p>Modest Impact</p>
<p>Modular Security Requirement</p> <p>B9 <i>If random numbers are generated by the device in connection with security over sensitive data, the random number generator has been assessed to ensure it is generating numbers sufficiently unpredictable.</i></p>	<p>B9 <i>If random numbers are generated by the device in connection with security over sensitive data, the random number generator has been assessed to ensure it is generating numbers sufficiently unpredictable.</i></p>	

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PCI PTS Version 3.1	PCI PTS Version 4	Notes
<p>Modular Evaluation Vendor Questionnaire Total Questions: 3</p> <p>Modular Derived Test Requirements Total Tests: 5</p>	<p>Total Questions: 4</p>	<p>The additional questions are related to the random number generator and the seed for pseudo random number generator. Also, the applicability to open protocols.</p> <p>New guidance provided</p>
<p>Modular Security Requirement B10 <i>The device has characteristics that prevent or significantly deter the use of the device for exhaustive PIN determination.</i></p> <p>Modular Evaluation Vendor Questionnaire Total Questions: 2</p> <p>Modular Derived Test Requirements Total Tests: 3</p>	<p>B10 <i>The device has characteristics that prevent or significantly deter the use of the device for exhaustive PIN determination.</i></p> <p>Total Questions: 2</p>	<p>No new questions</p>
<p>Modular Security Requirement B11 <i>The key-management techniques implemented in the device conform to ISO 11568 and/or ANSI X9.24. Key-management techniques must support the ANSI TR-31 key derivation methodology or an equivalent methodology for maintaining the TDEA key bundle.</i></p> <p>Modular Evaluation Vendor Questionnaire Total Questions: 21</p> <p>Modular Derived Test Requirements Total Tests: 10</p>	<p>B11 <i>The key-management techniques implemented in the device conform to ISO 11568 and/or ANSI X9.24. Key-management techniques must support the ANSI TR-31 key derivation methodology or an equivalent methodology for maintaining the TDEA key bundle.</i></p> <p>Total Questions: 22</p>	<p>The V3.1 questions were numbered incorrectly so there were two #14 questions. So there were 21 in all.</p> <p>The one new question requires information about how the keys that are stored in the device are generated</p> <p>New guidance provided</p>

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PCI PTS Version 3.1	PCI PTS Version 4	Notes
<p>Modular Security Requirement</p> <p>B12 <i>The PIN-encryption technique implemented in the device is a technique included in ISO 9564.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 2</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 3</p>	<p>B12 <i>The PIN-encryption technique implemented in the device is a technique included in ISO 9564.</i></p> <p>Total Questions: 3</p>	<p>One new question:</p> <p>Q3</p> <p><i>All methods that the POI supports for external PIN transfer to other network nodes or devices or other subcomponents outside the area validated to requirement A1.</i></p> <p>Seems redundant with A1.</p>
<p>Modular Security Requirement</p> <p>B13 <i>It is not possible to encrypt or decrypt any arbitrary data using any PIN-encrypting key or key-encrypting key contained in the device.</i></p> <p><i>The device must enforce that data keys, key-encipherment keys, and PIN-encryption keys have different values.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 8</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 4</p>	<p>B13 <i>It is not possible to encrypt or decrypt any arbitrary data using any PIN-encrypting key or key-encrypting key contained in the device.</i></p> <p><i>The device must enforce that data keys, key-encipherment keys, and PIN-encryption keys have different values.</i></p> <p>Total Questions: 9</p>	<p>The additional question requires specifics on the implementation if the device supports data decryption.</p>
<p>Modular Security Requirement</p> <p>B14 <i>There is no mechanism in the device that would allow the outputting of a private or secret clear-text key or clear-text PIN, the encryption of a key or PIN under a key that might itself be disclosed, or the transfer of a clear-text key from a component of high security into a component of lesser security.</i></p>	<p>B14 <i>There is no mechanism in the device that would allow the outputting of a private or secret clear-text key or clear-text PIN, the encryption of a key or PIN under a key that might itself be disclosed, or the transfer of a clear-text key from a component of high security into a component of lesser security.</i></p>	

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<p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 4</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 4</p>	<p>Total Questions: 4</p>	<p>No new questions</p>
<p>Modular Security Requirement</p> <p>B15 <i>The entry of any other transaction data must be separate from the PIN-entry process, avoiding the accidental display of a cardholder PIN on the device display. If other data and the PIN are entered on the same keypad, the other data entry and the PIN entry shall be clearly separate operations.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 3</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 3</p>	<p>B15 <i>The entry of any other transaction data must be separate from the PIN-entry process, avoiding the accidental display of a cardholder PIN on the device display. If other data and the PIN are entered on the same keypad, the other data entry and the PIN entry shall be clearly separate operations.</i></p> <p>Total Questions: 3</p>	<p>No new questions.</p>

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PCI PTS Version 3.1	PCI PTS Version 4	Notes
<p>Modular Security Requirement</p> <p>B16 <i>Note: If the POI device has a keypad that can be used to enter non-PIN data, the device must meet at least one of the following: A8, B16.1, B16.2, or E3.4.</i></p> <p><i>(Statements A8 and B16.1 are intended to be met by the vendor controlling the means of authorizing prompt changes. Statement B16.2 is an option that allows third parties to control the means of authorization. E3.4 is for all other unattended POI devices not meeting one of the aforementioned.)</i></p> <p>B16.1 <i>All prompts for non-PIN data entry are under the control of the cryptographic unit of the device and requiring an attack potential of at least 18 per device for identification and initial exploitation with a minimum of 9 for exploitation to circumvent. If the prompts are stored inside the cryptographic unit, they cannot feasibly be altered without causing the erasure of the unit's cryptographic keys. If the prompts are stored outside the cryptographic unit, cryptographic mechanisms must exist to ensure the authenticity and the proper use of the prompts and that modification of the prompts or improper use of the prompts is prevented.</i></p>	<p>B16 <i>Note: If the POI device has a keypad that can be used to enter non-PIN data, the device must meet at least one of the following: A7, B16, or E3.4.</i></p> <p><i>A7 applies to any components or paths containing plaintext display signals between the cryptographic processor and display unit.</i></p> <p><i>B16 applies to devices that allow for updates of prompts or use cryptography to communicate with a display, whether performed by the vendor or the acquirer.</i></p> <p><i>E3.4 is appropriate for unattended devices that do not meet any of the aforementioned.</i></p> <p><i>All prompts for non-PIN data entry are under the control of the cryptographic unit of the device and requiring an attack potential of at least 18 per device for identification and initial exploitation with a minimum of 9 for exploitation to circumvent. If the prompts are stored inside the cryptographic unit, they cannot feasibly be altered without causing the erasure of the unit's cryptographic keys. If the prompts are stored outside the cryptographic unit, cryptographic mechanisms must exist to ensure the authenticity and the proper use of the prompts and that modification of the prompts or improper use of the prompts is prevented.</i></p>	<p>The overall requirement is the same. B16.1 and B16.2 was combined as they were redundant. Devices that allowed the changing of prompts operated in the same mode whether or not the acquirer or the vendor controlled the prompts.</p>

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<p>B16.2 <i>Cryptographically based controls are utilized to control the device display and device usage such that it is infeasible for an entity not possessing the unlocking mechanism to alter the display and to allow the output of unencrypted PIN data from the device. The controls provide for unique accountability and utilize key sizes appropriate for the algorithm(s) in question. Key-management techniques and other control mechanisms are defined and include appropriate application of the principles of dual control and split knowledge.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 9 (total)</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 15 (total)</p>	<p>Total Questions: 8</p>	<p>No new questions</p> <p>New guidance provided</p>
<p>Modular Security Requirement</p> <p>B17 <i>If the device supports multiple applications, it must enforce the separation between applications. It must not be possible that one application interferes with or tampers with another application or the OS of the device, including modifying data objects belonging to another application.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 3</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 4</p>	<p>B17 <i>If the device supports multiple applications, it must enforce the separation between applications. It must not be possible that one application interferes with or tampers with another application or the OS of the device including, but not limited to, modifying data objects belonging to another application or the OS.</i></p> <p>Total Questions: 5</p>	<p>Same overall requirement with additional guidance.</p> <p>The additional questions are details about the application separation.</p> <p>Significant new guidance provided</p>
<p>Modular Security Requirement</p> <p>B18 <i>The operating system of the device must contain only the software (components and services) necessary for the intended operation. The operating system must be configured securely and run with least privilege.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 4</p>	<p>B18 <i>The operating system of the device must contain only the software (components and services) necessary for the intended operation. The operating system must be configured securely and run with least privilege.</i></p> <p>Total Questions: 6</p>	<p>Additional questions about the operating system (commercial or custom) and the support of default API and functions.</p>

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PCI PTS Version 3.1	PCI PTS Version 4	Notes
<p>Modular Derived Test Requirements</p> <p>Total Tests: 5</p>		
<p>Modular Security Requirement</p> <p>B19 <i>The vendor must provide adequate documented security guidance for the integration of any secure component into a PIN entry POI Terminal.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 2</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 4</p>	<p>B19 <i>The vendor must provide adequate documented security guidance for the integration of any secure component into a PIN entry POI Terminal.</i></p> <p>Total Questions: 2</p>	<p>No new questions.</p>
<p>Modular Security Requirement</p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 0</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 0</p>	<p>B20 <i>A user-available security policy from the vendor addresses the proper use of the POI in a secure fashion, including information on key-management responsibilities, administrative responsibilities, device functionality, identification, and environmental requirements. The security policy must define the roles supported by the POI and indicate the services available for each role in a deterministic tabular format. The POI is capable of performing only its designed functions— i.e., there is no hidden functionality. The only approved functions performed by the POI are those allowed by the policy.</i></p> <p>Total Questions: 2</p>	<p>This is a new requirement that requires the vendor to provide complete documentation on the secure use of the product. This will have to be provided to the evaluation lab to be marked “Yes”</p> <p>Questions on how the security policy is documented and how the device complies with the policy.</p> <p>New guidance provided</p>

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C – Online Security Requirements

Requirement Alignment Table

PCI PTS Version 3.1	PCI PTS Version 4	Notes
<p>Modular Security Requirement</p> <p>C1 <i>If the device can hold multiple PIN-encryption keys and if the key to be used to encrypt the PIN can be externally selected, the device prohibits unauthorized key replacement and key misuse.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 3</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 2</p>	<p>C1 <i>If the device can hold multiple PIN-encryption keys and if the key to be used to encrypt the PIN can be externally selected, the device prohibits unauthorized key replacement and key misuse.</i></p> <p>Total Questions: 4</p>	<p>Additional questions providing information about the key hierarchies.</p>

D – Offline Security Requirements

Requirement Alignment Table

PCI PTS Version 3.1	PCI PTS Version 4	Notes
<p>Modular Security Requirement</p> <p>D1 <i>It is neither feasible to penetrate the ICC reader to make any additions, substitutions, or modifications to either the ICC reader's hardware or software, in order to determine or modify any sensitive data, without requiring an attack potential of at least 20 for identification and initial exploitation, with a minimum of 10 for exploitation, nor is it possible for both an ICC card and any other foreign object to reside within the card insertion slot.</i></p> <p><i>Note: The card reader may consist of areas of different protection levels, e.g., the areas of the ICC card interface itself, and the area holding retracted cards.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 3</p>	<p>D1 <i>It is neither feasible to penetrate the ICC reader to make any additions, substitutions, or modifications to either the ICC reader's hardware or software, in order to determine or modify any sensitive data, without requiring an attack potential of at least 20 for identification and initial exploitation, with a minimum of 10 for exploitation, nor is it possible for both an ICC card and any other foreign object to reside within the card insertion slot.</i></p> <p>Note: All attacks shall include a minimum of ten hours' attack time for exploitation.</p> <p>Total Questions: 19</p>	<p>The ten hour minimum for exploitation is finally included as a specific requirement. This has been a requirement for scoring the attack potential, but one would have to refer to the Technical FAQ.</p> <p>The majority of the questions added to D1 are actually from the V3.1 D2 questionnaire. There is no explanation why they were moved.</p>

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PCI PTS Version 3.1	PCI PTS Version 4	Notes
<p>Modular Derived Test Requirements</p> <p>Total Tests: 8</p>		
<p>Modular Security Requirement</p> <p>D2 <i>The opening for the insertion of the IC card is in full view of the cardholder during card insertion so that any untoward obstructions or suspicious objects at the opening are detectable.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 11</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 2</p>	<p>D2 <i>The opening for the insertion of the IC card is in full view of the cardholder during card insertion so that any untoward obstructions or suspicious objects at the opening are detectable.</i></p> <p>Total Questions: 1</p>	<p>The missing questions from V4 were moved to D1. There is no explanation why they were moved.</p>
<p>Modular Security Requirement</p> <p>D3 <i>The ICC reader is constructed so that wires running out of the slot of the IC reader to a recorder or a transmitter (an external bug) can be observed by the cardholder.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 2</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 2</p>	<p>D3 <i>The ICC reader is constructed so that wires running out of the slot of the IC reader to a recorder or a transmitter (an external bug) can be observed by the cardholder.</i></p> <p>Total Questions: 2</p>	<p>No new questions.</p>
<p>Modular Security Requirement</p> <p>D4 <i>PIN protection during transmission between the device encrypting the PIN and the ICC Reader (at least two must apply):</i></p>	<p>D4 <i>PIN protection during transmission between the device encrypting the PIN and the ICC reader (at least two must apply):</i></p> <p><i>If the device encrypting the PIN and the ICC reader are not integrated into the same secure module, and the cardholder verification method is determined to be:</i></p> <ul style="list-style-type: none"> <i>An enciphered PIN, the PIN block shall be enciphered between the device encrypting the PIN and the ICC reader using either an authenticated encipherment key of the IC card, or in accordance with ISO 9564.</i> <i>A plaintext PIN, the PIN block shall be enciphered</i> 	<p>Overall the same requirement, D4.1 to D4.4 consolidated to one requirement.</p>

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PCI PTS Version 3.1	PCI PTS Version 4	Notes
<p>D4.1 <i>If the device encrypting the PIN and the ICC reader are not integrated into the same secure module, and the cardholder verification method is determined to be an enciphered PIN, the PIN block shall be enciphered between the device encrypting the PIN and the ICC reader using either an authenticated encipherment key of the IC card, or in accordance with ISO 9564</i></p> <p>D4.2 <i>If the device encrypting the PIN and the ICC reader are not integrated into the same secure module, and the cardholder verification method is determined to be a plain-text PIN, the PIN block shall be enciphered from the device encrypting the PIN to the ICC reader (the ICC reader will then decipher the PIN for transmission in plain-text to the IC card) in accordance with ISO 9564</i></p> <p>D4.3 <i>If the device encrypting the PIN and the ICC reader are integrated into the same secure module, and the cardholder verification method is determined to be an enciphered PIN, the PIN block shall be enciphered using an authenticated encipherment key of the IC card.</i></p> <p>D4.4 <i>If the device encrypting the PIN and the ICC reader are integrated into the same secure module, and the cardholder verification method is determined to be a plain-text PIN, then encipherment is not required if the PIN block is transmitted wholly through a protected environment (as defined in ISO 9564). If the plain-text PIN is transmitted to the ICC reader through an unprotected environment, then the PIN block shall be</i></p>	<p><i>from the device encrypting the PIN to the ICC reader (the ICC reader will then decipher the PIN for transmission in plaintext to the IC card) in accordance with ISO 9564.</i></p> <p><i>If the device encrypting the PIN and the ICC reader are integrated into the same secure module, and the cardholder verification method is determined to be:</i></p> <ul style="list-style-type: none"> • <i>An enciphered PIN, the PIN block shall be enciphered using an authenticated encipherment key of the IC card.</i> • <i>A plaintext PIN, then encipherment is not required if the PIN block is transmitted wholly through a protected environment (as defined in ISO 9564). If the plaintext PIN is transmitted to the ICC reader through an unprotected environment, the PIN block shall be enciphered in accordance with ISO 9564.</i> 	

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<i>enciphered in accordance with ISO 9564.</i> Modular Evaluation Vendor Questionnaire Total Questions: 5 Modular Derived Test Requirements Total Tests: 9 (total)	Total Questions: 4	No new questions.

Evaluation Module 2: POS Terminal Integration

E – POS Terminal Integration Security Requirements

No significant changes.

Evaluation Module 3: Open Protocols

F – POS Terminal Integration Security Requirements

There are no changes to these requirements, they have just be re-aligned.

Evaluation Module 4: Secure Reading and Exchange of Data (SRED)

Account Data Protection

Requirement Alignment Table

PCI PTS Version 3.1	PCI PTS Version 4	Notes
Modular Security Requirement K1 <i>All account data is either encrypted immediately upon entry or entered in clear-text into a secure device and processed within the secure controller of the device.</i> Modular Evaluation Vendor Questionnaire Total Questions: 4	K1 <i>All account data is either encrypted immediately upon entry or entered in clear-text into a secure device and processed within the secure controller of the device.</i> Total Questions: 4	No new questions

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PCI PTS Version 3.1	PCI PTS Version 4	Notes
<p>Modular Derived Test Requirements</p> <p>Total Tests: 3</p>		
<p>Modular Security Requirement</p> <p>K1.1 <i>The device protects all account data upon entry (consistent with A10 for magnetic stripe data and D1 for Chip data), and there is no method of accessing the clear-text account data (using methods described in A1) without defeating the security of the device. Defeating or circumventing the security mechanism requires an attack potential of at least 16 for identification and initial exploitation, with a minimum of 8 for exploitation</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 22 (total)</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 5</p>	<p>K1.1 <i>The device protects all account data upon entry (consistent with A10 for magnetic stripe data and D1 for Chip data), and there is no method of accessing the clear-text account data (using methods described in A1) without defeating the security of the device. Defeating or circumventing the security mechanism requires an attack potential of at least 16 for identification and initial exploitation, with a minimum of 8 for exploitation</i></p> <p><i>Note: MSRs and ICCRs must meet the attack potentials stipulated in DTRs A10 and D1 respectively.</i></p> <p>Total Questions: 34 (total)</p>	<p>There is a discrepancy here, K1.1 provides a specific attack potential that must be met. However the note indicates that the attack potentials are to be consistent with A10 and D1.</p> <p>A10 is the requirement for the magnetic stripe reader:</p> <ul style="list-style-type: none"> Attack potential is 18 points, minimum of 9 points for exploitation. <p>D1 is the requirement for the contact chip card reader:</p> <ul style="list-style-type: none"> Attack potential is 20 points, minimum of 10 points for exploitation. <p>For devices that are PEDs these questions are consistent with various requirements in sections A-D. The duplication of the questions are for the devices that those devices that are not PIN Entry Devices and seek SRED approval.</p>
<p>Modular Security Requirement</p>	<p>K1.2 <i>Failure of a single security mechanism does not compromise device security. Protection against a threat is based on a combination of at least two independent security mechanisms.</i></p>	<p>This is language consistent with A2 (A1.2 in version 3).</p> <p>This is potentially a major issue to meet SRED depending on the implementation that will require a reader redesign. In the key pad requirement, this meant that there was more than one active security mechanism that had</p>

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PCI PTS Version 3.1	PCI PTS Version 4	Notes
<p>Modular Evaluation Vendor Questionnaire Total Questions: 0</p> <p>Modular Derived Test Requirements Total Tests:</p>	<p>Total Questions: 4</p>	<p>to be defeated. Many reader implementation cannot meet this at this time, especially those used in modular POI devices.</p> <p>In the case of an encrypting heads, there is only one security mechanism in that the firmware and key are injected at the time of manufacturing and are typically a onetime event. These devices do not typically have battery backed RAM to store secrets.</p> <p>At this time the requirement is incomplete due to a lack of a version 4 technical FAQ.</p> <p>New guidance provided</p>
<p>Modular Security Requirement</p> <p>K2 <i>The logical and physical integration of an approved secure card reader into a PIN entry POI terminal does not create new attack paths to the account data. The account data is protected (consistent with A2) from the input component to the secure controller of the device.</i></p> <p>Modular Evaluation Vendor Questionnaire Total Questions: 8</p> <p>Modular Derived Test Requirements Total Tests: 4</p>	<p>K2 <i>The logical and physical integration of an approved secure card reader into a PIN entry POI terminal does not create new attack paths to the account data. The account data is protected (consistent with A2) from the input component to the secure controller of the device.</i></p> <p>Total Questions: 8</p>	<p>No new questions</p> <p>New guidance provided</p>
<p>Modular Security Requirement</p> <p>K3 <i>Determination of any cryptographic keys used for account data encryption, by penetration of the device and/or by monitoring emanations from the device (including power fluctuations), requires an attack potential of at least 26 for identification and initial exploitation with a minimum of 13 for exploitation</i></p>	<p>K3 <i>Determination of any cryptographic keys used for account data encryption, by penetration of the device and/or by monitoring emanations from the device (including power fluctuations), requires an attack potential of at least 26 for identification and initial exploitation with a minimum of 13 for exploitation.</i></p>	

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PCI PTS Version 3.1	PCI PTS Version 4	Notes
<p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 5</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 3</p>	<p>Total Questions: 7</p>	<p>See A7.</p> <p>New guidance provided</p>
<p>Modular Security Requirement</p> <p>K3.1 <i>Public keys must be stored and used in a manner that protects against unauthorized modification or substitution. Unauthorized modification or substitution requires an attack potential of at least 26 for identification and initial exploitation with a minimum of 13 for exploitation</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 3</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 3</p>	<p>K3.1 <i>Public keys must be stored and used in a manner that protects against unauthorized modification or substitution. Unauthorized modification or substitution requires an attack potential of at least 26 for identification and initial exploitation with a minimum of 13 for exploitation</i></p> <p>Total Questions: 3</p>	<p>No new tests</p>
<p>Modular Security Requirement</p> <p>K4 <i>All account data shall be encrypted using only ANSI X9 or ISO-approved encryption algorithms (e.g., AES, TDES) and should use ANSI X9 or ISO-approved modes of operation.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 7</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 6</p>	<p>K4 <i>All account data shall be encrypted using only ANSI X9 or ISO-approved encryption algorithms (e.g., AES, TDES) and should use ANSI X9 or ISO-approved modes of operation.</i></p> <p>Total Questions: 7</p>	<p>No new questions</p>
<p>Modular Security Requirement</p> <p>K5 <i>If remote key distribution is used, the device supports mutual authentication between the sending key distribution host and receiving device.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 5</p>	<p>K5 <i>If remote key distribution is used, the device supports mutual authentication between the sending key distribution host and receiving device.</i></p> <p>Total Questions: 5</p>	<p>No new questions</p>

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PCI PTS Version 3.1	PCI PTS Version 4	Notes
<p>Modular Derived Test Requirements</p> <p>Total Tests: 4</p>		
<p>Modular Security Requirement</p> <p>K6 <i>The device supports data origin authentication of encrypted messages.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 1</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 2</p>	<p>K6 <i>The device supports data origin authentication of encrypted messages.</i></p> <p>Total Questions: 1</p>	No new questions
<p>Modular Security Requirement</p> <p>K7 <i>Secret and private keys which reside within the device to support account data encryption are unique per device.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 1</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 3</p>	<p>K7 <i>Secret and private keys that reside within the device to support account data encryption are unique per device.</i></p> <p>Total Questions: 1</p>	No new questions
<p>Modular Security Requirement</p> <p>K8 <i>Encryption or decryption of any arbitrary data using any account data-encrypting key or key-encrypting key contained in the device is not permitted.</i></p> <p><i>The device must enforce that account data keys, key-encipherment keys, and PIN-encryption keys have different values.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 8</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 4</p>	<p>K8 <i>Encryption or decryption of any arbitrary data using any account data-encrypting key or key-encrypting key contained in the device is not permitted.</i></p> <p><i>The device must enforce that account data keys, key-encipherment keys, and PIN-encryption keys have different values.</i></p> <p>Total Questions: 8</p>	<p>No new questions, minor change to question 8 about unique keys.</p> <p>New guidance provided</p>

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PCI PTS Version 3.1	PCI PTS Version 4	Notes
<p>Modular Security Requirement</p> <p>K9 <i>If the device may be accessed remotely for the purposes of administration, all access attempts must be cryptographically authenticated. If the authenticity of the access request cannot be confirmed, the access request is denied.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 7</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 9</p>	<p>K9 <i>If the device may be accessed remotely for the purposes of administration, all access attempts must be cryptographically authenticated. If the authenticity of the access request cannot be confirmed, the access request is denied.</i></p> <p>Total Questions: 7</p>	<p>No new questions</p>
<p>Modular Security Requirement</p> <p>K10 <i>The firmware, and any changes thereafter, have been inspected and reviewed consistent with B3.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 2</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 4</p>	<p>K10 <i>The firmware, and any changes thereafter, have been inspected and reviewed consistent with B3.</i></p> <p>Total Questions: 4</p>	<p>See B3 above</p> <p>New guidance provided</p>
<p>Modular Security Requirement</p> <p>K11 <i>The device performs self-tests consistent with B1.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 8</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 4</p>	<p>Total Questions: 0</p>	<p>Removed since K11.1 and K11.2 specify the requirements they must be consistent with.</p>
<p>Modular Security Requirement</p> <p>K11.1 <i>The firmware must confirm the authenticity of all applications loaded onto the terminal consistent with B4. If the device allows software application and/or configuration updates, the device cryptographically authenticates all updates consistent with B4.</i></p>	<p>K11.1 <i>The firmware must confirm the authenticity of all applications loaded onto the terminal consistent with B4. If the device allows software application and/or configuration updates, the device cryptographically authenticates all updates consistent with B4.</i></p>	

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PCI PTS Version 3.1	PCI PTS Version 4	Notes
<p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 9 (3 of which were unnumbered)</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 7</p>	<p>Total Questions: 9</p>	<p>No new questions.</p>
<p>Modular Security Requirement</p> <p>K11.2 <i>The vendor must provide clear security guidance consistent with B2 and B6 to all application developers to ensure:</i></p> <ul style="list-style-type: none"> <i>That it is not possible for applications to be influenced by logical anomalies which could result in clear text data being outputted whilst the terminal is in encrypting mode.</i> <i>That account data is not retained any longer, or used more often, than strictly necessary.</i> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 1</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 3</p>	<p>K11.2 <i>The vendor must provide clear security guidance consistent with B2 and B6 to all application developers to ensure:</i></p> <ul style="list-style-type: none"> <i>That it is not possible for applications to be influenced by logical anomalies which could result in clear text data being outputted whilst the terminal is in encrypting mode.</i> <i>That account data is not retained any longer, or used more often, than strictly necessary.</i> <p>Total Questions: 1</p>	<p>No new questions</p>
<p>Modular Security Requirement</p> <p>K12 <i>If the device allows updates of firmware, the device cryptographically authenticates the firmware and if the authenticity is not confirmed, the firmware update is rejected and deleted.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 5</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 7</p>	<p>K12 <i>If the device allows updates of firmware, the device cryptographically authenticates the firmware and if the authenticity is not confirmed, the firmware update is rejected and deleted.</i></p> <p>Total Questions: 7</p>	<p>See B4 above.</p>
<p>Modular Security Requirement</p> <p>K13 <i>The device's functionality shall not be influenced by logical anomalies such as (but not limited to) unexpected command sequences, unknown commands, commands in a wrong device mode and supplying</i></p>	<p>K13 <i>The device's functionality shall not be influenced by logical anomalies consistent with B2.</i></p>	

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PCI PTS Version 3.1	PCI PTS Version 4	Notes
<p><i>wrong parameters or data which could result in the device outputting clear-text account data.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 6</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 4</p>	<p>Total Questions: 10</p>	<p>See B2 above</p> <p>New guidance provided</p>
<p>Modular Security Requirement</p> <p>K14 <i>The security requirements specified in sections H and J of the Open Protocols module have been met.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 1</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 2</p>	<p>K14 <i>If the device is capable of communicating over an IP network or uses a public domain protocol (such as but not limited to Wi-Fi or Bluetooth), then requirements specified in DTR Module 3: Open Protocols Requirements have been met.</i></p> <p>Total Questions: 1</p>	<p>Based on the change to open protocols module, K14 and K15 have been combined to one requirement</p> <p>No new questions.</p>
<p>Modular Security Requirement</p> <p>K15 <i>If the device is capable of communicating over an IP network, the security requirements specified in sections F, G, and I of the Open Protocols module have been met.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 1</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 2</p>	<p>Total Questions: 0</p>	
<p>Modular Security Requirement</p> <p>K16 <i>When operating in encrypting mode, there is no mechanism in the device that would allow the outputting of clear-text account data. Changing between an encrypting and non-encrypting mode of operation requires explicit authentication.</i></p>	<p>K15 <i>When operating in encrypting mode, there is no mechanism in the device that would allow the outputting of clear-text account data. Changing between an encrypting and non-encrypting mode of operation requires explicit authentication.</i></p>	<p>Same requirement, different requirement reference.</p>

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PCI PTS Version 3.1	PCI PTS Version 4	Notes
<p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 8</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 8</p>	<p>Total Questions: 8</p>	<p>No new questions</p> <p>New guidance provided</p>
<p>Modular Security Requirement</p> <p>K16.1 <i>When operating in encrypting mode, the secure controller can only release clear-text account data to authenticated applications executing within the device.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 2</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 3</p>	<p>K15.1 <i>When operating in encrypting mode, the secure controller can only release clear-text account data to authenticated applications executing within the device.</i></p> <p>Total Questions: 2</p>	<p>Same requirement, different requirement reference.</p> <p>No new tests</p>
<p>Modular Security Requirement</p> <p>K16.2 <i>Account data (in either clear-text or encrypted form) shall not be retained any longer, or used more often, than strictly necessary.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 9</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 4</p>	<p>K15.2 <i>Account data (in either clear-text or encrypted form) shall not be retained any longer, or used more often, than strictly necessary.</i></p> <p>Total Questions: 9</p>	<p>Same requirement, different requirement reference.</p> <p>No new tests</p>
<p>Modular Security Requirement</p> <p>K17 <i>If the device is capable of generating surrogate PAN values to be outputted outside of the device, it is not possible to determine the original PAN knowing only the surrogate value.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 2</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 3</p>	<p>K16 <i>If the device is capable of generating surrogate PAN values to be outputted outside of the device, it is not possible to determine the original PAN knowing only the surrogate value.</i></p> <p>Total Questions: 2</p>	<p>Same requirement, different requirement reference.</p> <p>No new tests</p>

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PCI PTS Version 3.1	PCI PTS Version 4	Notes
<p>Modular Security Requirement</p> <p>K17.1 <i>If using a hash function to generate surrogate PAN values, input to the hash function must use a salt with minimum length of 64-bits.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 2</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 4</p>	<p>K16.1 <i>If using a hash function to generate surrogate PAN values, input to the hash function must use a salt with minimum length of 64-bits.</i></p> <p>Total Questions: 2</p>	<p>Same requirement, different requirement reference.</p> <p>No new questions</p>
<p>Modular Security Requirement</p> <p>K17.2 <i>If using a hash function to generate surrogate PAN values, the salt is kept secret and appropriately protected. Disclosure of the salt cannot occur without requiring an attack potential of at least 16 per device for identification and initial exploitation with a minimum of 8 for exploitation</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 1</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 3</p>	<p>K16.2 <i>If using a hash function to generate surrogate PAN values, the salt is kept secret and appropriately protected. Disclosure of the salt cannot occur without requiring an attack potential of at least 16 per device for identification and initial exploitation with a minimum of 8 for exploitation</i></p> <p>Total Questions: 1</p>	<p>Same requirement, different requirement reference.</p> <p>No new questions</p>
<p>Modular Security Requirement</p> <p>K18 <i>The key-management techniques implemented in the device are consistent with B11.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 20</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 10</p>	<p>K17 <i>The key-management techniques implemented in the device are consistent with B11.</i></p> <p>Total Questions: 22</p>	<p>Same requirement, different requirement reference.</p> <p>See A6 above</p>
<p>Modular Security Requirement</p> <p>K19 <i>The device has characteristics that prevent or significantly deter the use of the device for exhaustive PAN determination.</i></p>	<p>K18 <i>The device has characteristics that prevent or significantly deter the use of the device for exhaustive PAN determination.</i></p>	<p>Same requirement, different requirement reference.</p>

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PCI PTS Version 3.1	PCI PTS Version 4	Notes
<p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 1</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 3</p>	<p>Total Questions: 1</p>	<p>No new questions</p>
<p>Modular Security Requirement</p> <p>K20 <i>If the device permits access to internal areas (e.g., for service or maintenance), it is not possible using this access area to insert a bug that would disclose any secret or private keys or account data. Immediate access to secret or private keys or account data is either prevented by the design of the internal areas (e.g., by enclosing components with such data into tamper-resistant/responsive enclosures), and/or it has a mechanism so that accessing internal areas causes the immediate erasure of secret and private keys and account data.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 6</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 8</p>	<p>Total Questions: 0</p>	<p>This requirement was removed because it was redundant with other tests.</p>
<p>Modular Security Requirement</p> <p>K21 <i>Environmental or operational conditions cannot be altered to compromise the security of the device, or cause the device to output clear-text account data.</i></p> <p><i>(An example includes subjecting the device to temperatures or operating voltages outside the stated operating ranges.)</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 4</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 4</p>	<p>K19 <i>Environmental or operational conditions cannot be altered to compromise the security of the device, or cause the device to output clear-text account data.</i></p> <p><i>(An example includes subjecting the device to temperatures or operating voltages outside the stated operating ranges.)</i></p> <p>Total Questions: 6</p>	<p>Same requirement, different requirement reference.</p> <p>See A3 above</p>
<p>Modular Security Requirement</p> <p>K22 <i>If the device supports multiple applications, it must enforce the separation between applications consistent</i></p>	<p>K20 <i>If the device supports multiple applications, it must enforce the separation between applications consistent</i></p>	<p>Same requirement, different requirement</p>

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PCI PTS Version 3.1	PCI PTS Version 4	Notes
<p><i>with B17.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 3</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 4</p>	<p><i>with B17</i></p> <p>Total Questions: 5</p>	<p>reference.</p> <p>Additional information about application security and separation.</p>
<p>Modular Security Requirement</p> <p>K23 <i>The following features of the device's operating system must be in place:</i></p> <ul style="list-style-type: none"> <i>The operating system of the device must contain only the software (components and services) necessary for the intended operation.</i> <i>The operating system must be configured securely and run with least privilege.</i> <i>The security policy enforced by the device must not allow unauthorized or unnecessary functions.</i> <i>API functionality and commands that are not required to support specific functionality must be disabled (and where possible, removed).</i> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 6</p> <p>Modular Derived Test Requirements</p> <p>Total Tests: 7</p>	<p>K21 <i>The following features of the device's operating system must be in place:</i></p> <ul style="list-style-type: none"> <i>The operating system of the device must contain only the software (components and services) necessary for the intended operation.</i> <i>The operating system must be configured securely and run with least privilege.</i> <i>The security policy enforced by the device must not allow unauthorized or unnecessary functions.</i> <i>API functionality and commands that are not required to support specific functionality must be disabled (and where possible, removed).</i> <p>Total Questions: 7</p>	<p>Same requirement, different requirement reference.</p> <p>See B18 above</p>
<p>Modular Security Requirement</p> <p>K24 <i>Access to sensitive services requires authentication. Sensitive services provide access to the underlying sensitive functions. Sensitive functions are those functions that process sensitive data such as cryptographic keys, account data, and passwords. Entering or exiting sensitive services shall not reveal or otherwise affect sensitive data.</i></p> <p>Modular Evaluation Vendor Questionnaire</p> <p>Total Questions: 10</p>	<p>K22 <i>Access to sensitive services requires authentication. Sensitive services provide access to the underlying sensitive functions. Sensitive functions are those functions that process sensitive data such as cryptographic keys, account data, and passwords. Entering or exiting sensitive services shall not reveal or otherwise affect sensitive data.</i></p> <p>Total Questions: 11</p>	<p>Same requirement, different requirement reference.</p> <p>See B7 above</p>

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PCI PTS Version 3.1	PCI PTS Version 4	Notes
Modular Derived Test Requirements Total Tests: 6		
Modular Security Requirement K25 <i>Sensitive services are protected from unauthorized use consistent with B8.</i> Modular Evaluation Vendor Questionnaire Total Questions: 8 Modular Derived Test Requirements Total Tests: 6	K23 <i>Sensitive services are protected from unauthorized use consistent with B8.</i> Total Questions: 8	Same requirement, different requirement reference. See B8 above

Summary of Additional Questions and Test

A – Core Physical Security Requirements

V4 Requirement	Questionnaires			DTR Tests		
	V3	V4	% Increase	V3	V4	% Increase
A1	9	20	122%	6	32	433%
A2	4	4	0%	3	4	33%
A3	3	5	67%	4	10	150%
A4	6	11	83%	4	13	225%
A5	3	3	0%	3	4	33%
A6	6	6	0%	5	10	100%
A7	5	6	20%	3	15	400%
A8	3	4	33%	3	7	133%
A9	2	3	50%	4	12	200%
A10	4	6	50%	5	9	80%
A11	7	8	14%	6	12	100%
	52	76	46%	46	128	178%

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B – Core Logical Security Requirements

V4 Requirement	Questionnaires			V3	DTR Tests		
	V3	V4	% Increase		V4	% Increase	
B1	4	8	100%	4	14	250%	
B2	6	10	67%	4	11	175%	
B3	2	4	100%	4	19	375%	
B4	5	7	40%	7	14	100%	
B4.1		9			14		
B5	2	2	0%	4	8	100%	
B6	10	10	0%	4	8	100%	
B7	10	11	10%	8	18	125%	
B8	8	8	0%	6	9	50%	
B9	3	4	33%	5	6	20%	
B10	2	2	0%	3	8	167%	
B11	21	22	5%	10	26	160%	
B12	2	3	50%	3	9	200%	
B13	8	9	13%	4	5	25%	
B14	4	4	0%	4	7	75%	
B15	3	3	0%	3	4	33%	
B16	9	8	-11%	15	13	-13%	
B17	3	5	67%	4	14	250%	
B18	4	6	50%	5	8	60%	
B19	2	2	0%	4	4	0%	
B20		2			19		
	108	139	29%	101	238	136%	

C – Online Security Requirements

V4 Requirement	Questionnaires			V3	DTR Tests	
	V3	V4	% Increase		V4	% Increase
C1	3	4	33%	2	5	150%
	3	4	33%	2	5	150%

D – Offline Security Requirements

Questionnaires

DTR Tests

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V4 Requirement	V3	V4	% Increase	V3	V4	% Increase
D1	3	19	533%	8	19	138%
D2	11	1	-91%	2	3	50%
D3	2	2	0%	2	6	200%
D4	5	4	-20%	9	11	22%
	21	26	24%	21	39	86%

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Account Data Protection

V4 Requirement	Questionnaires			DTR Tests		
	V3	V4	% Increase	V3	V4	% Increase
K1	4	4	0%	3	3	0%
K1.1	22	34	55%	5	5	0%
K1.2		4			4	
K2	8	8	0%	4	4	0%
K3	5	7	40%	3	14	367%
K3.1	3	3	0%	3	3	0%
K4	7	7	0%	6	6	0%
K5	5	5	0%	4	4	0%
K6	1	1	0%	2	2	0%
K7	1	1	0%	3	4	33%
K8	8	8	0%	4	4	0%
K9	7	7	0%	9	9	0%
K10	2	4	100%	4	19	375%
K11	8	0	-100%	4	0	-100%
K11.1	9	9	0%	7	14	100%
K11.2	1	1	0%	3	3	0%
K12	5	7	40%	7	14	100%
K13	6	10	67%	4	14	250%
K14	1	1	0%	2	2	0%
K15	1	0	-100%	2	0	-100%
K16	8	8	0%	8	8	0%
K16.1	2	2	0%	3	3	0%
K16.2	9	9	0%	4	8	100%
K17	2	2	0%	3	3	0%
K17.1	2	2	0%	4	4	0%
K17.2	1	1	0%	3	3	0%
K18	20	22	10%	10	25	150%
K19	1	1	0%	3	3	0%
K20	6			8		
K21	4	6	50%	4	10	150%
K22	3	5	67%	4	12	200%
K23	6	7	17%	7	9	29%
K24	10	11	10%	6	16	167%
K25	8	8	0%	6	9	50%

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186	205	10%
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152	241	59%
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Totals

Total

Questionnaires		
V3	V4	% Increase
370	450	22%

DTR Tests		
V3	V4	% Increase
322	651	102%

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Summary

This paper is not meant to bash the PCI SSC in anyway. We actually appreciate the program because it sets the security bar and levels the playing field for all secure payment terminal vendors. However, we have a long history with PED security and have watched the evaluation from the Visa PED vendor self attestation to the Visa PED lab evaluation to the introduction of PCI to finally where we are today.

By now you are probably wondering about the title of this white paper *PCI PTS V3.x to V4.0 Gap Analysis, But WTF*. The actual title of this paper is:

*PCI PTS V3.x to V4.0 Gap Analysis, But **Where's the Technical FAQ***.

For those that have been involved with the PCI program for any amount of time know that the true requirements are not in the security requirements, questionnaire or even the DTRs, they are actually refined in the Technical FAQs. And the technical FAQs can be published at any time, and could when published have negatively affected a new product development cycle.

There are also some very vague security requirements hidden in plain sight of the details that may be difficult to address without the infamous technical FAQ.